

# UDOO X86 II SINGLE BOARD

These schematics are provided exclusively to support the receiver for its internal activities.

The schematics are provided "AS IS". SECO makes no representations about the suitability of these materials for any purpose and disclaims all warranties and conditions with regard to these materials, including but not limited to, all implied warranties and conditions of merchantability, fitness for a particular purpose, title and non-infringement of any third party intellectual property right.

You acknowledge and agree that these schematics are provided as an example only and that you will exercise your own independent analysis and judgment in your use of these materials. SECO assumes no liability for your use of these materials on your product design.

## CONFIGURATIONS :

### eMMC ON-BOARD

**A0** --> eMMC mounted

**A1** --> eMMC not mounted

### RAM ICs DENSITY

**B0** --> 1Gb ICs

**B1** --> 2Gb ICs

**B2** --> 4Gb ICs

**B3** --> 8Gb ICs

### SINGLE/DUAL CHANNEL RAM

**C0** --> DUAL CHANNEL

**C1** --> SINGLE CHANNEL

## ABSOLUTE MAXIMUM RATINGS

Voltage applied at Vin to GND..... -0.3 V to 13 V

## RECOMMENDED OPERATING CONDITIONS

Supply voltage..... 12V ± 5%

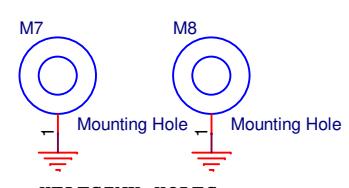
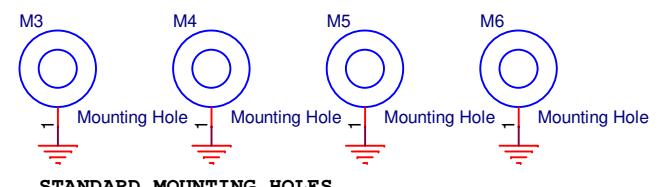
## Contents

Page 1	Main
Page 2	Block Diagram
Page 3	DDR Interface
Page 4	DDI CSI SDMMC Interfaces
Page 5	PCI-E SATA I2C HDA Interfaces
Page 6	USB UART Interfaces
Page 7	BRASWELL Power
Page 8	BRASWELL GND Pins
Page 9	DDR3L Channel 1
Page 10	DDR3L Channel 2
Page 11	POWER 1
Page 12	POWER 2
Page 13	POWER 3
Page 14	HDMI Gigabit Ethernet
Page 15	mini DP++ connectors
Page 16	USB 3.0 connectors
Page 17	Audio Codec + connectors
Page 18	ARDUINO Leonardo + Expansion connectors
Page 19	eMMC, microSD Slot, SATA connector
Page 20	M.2 Expansion Slots
Page 21	Glue Logic + FAN
Page 22	Front Panel, Brick, CIR, CSI

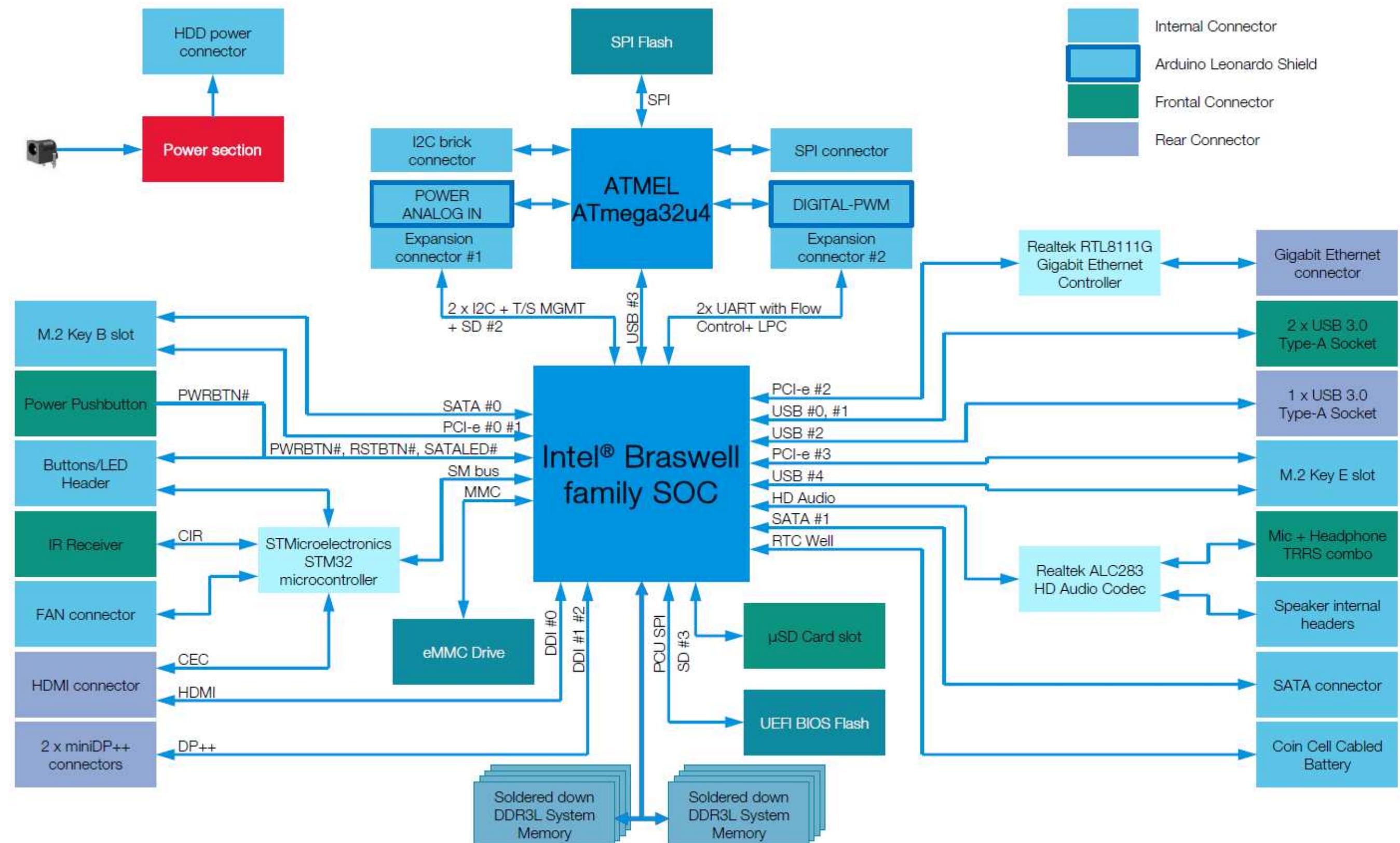
## Reference Table

Components with CFG = DNP indications are foreseen in the PCB but must not be mounted

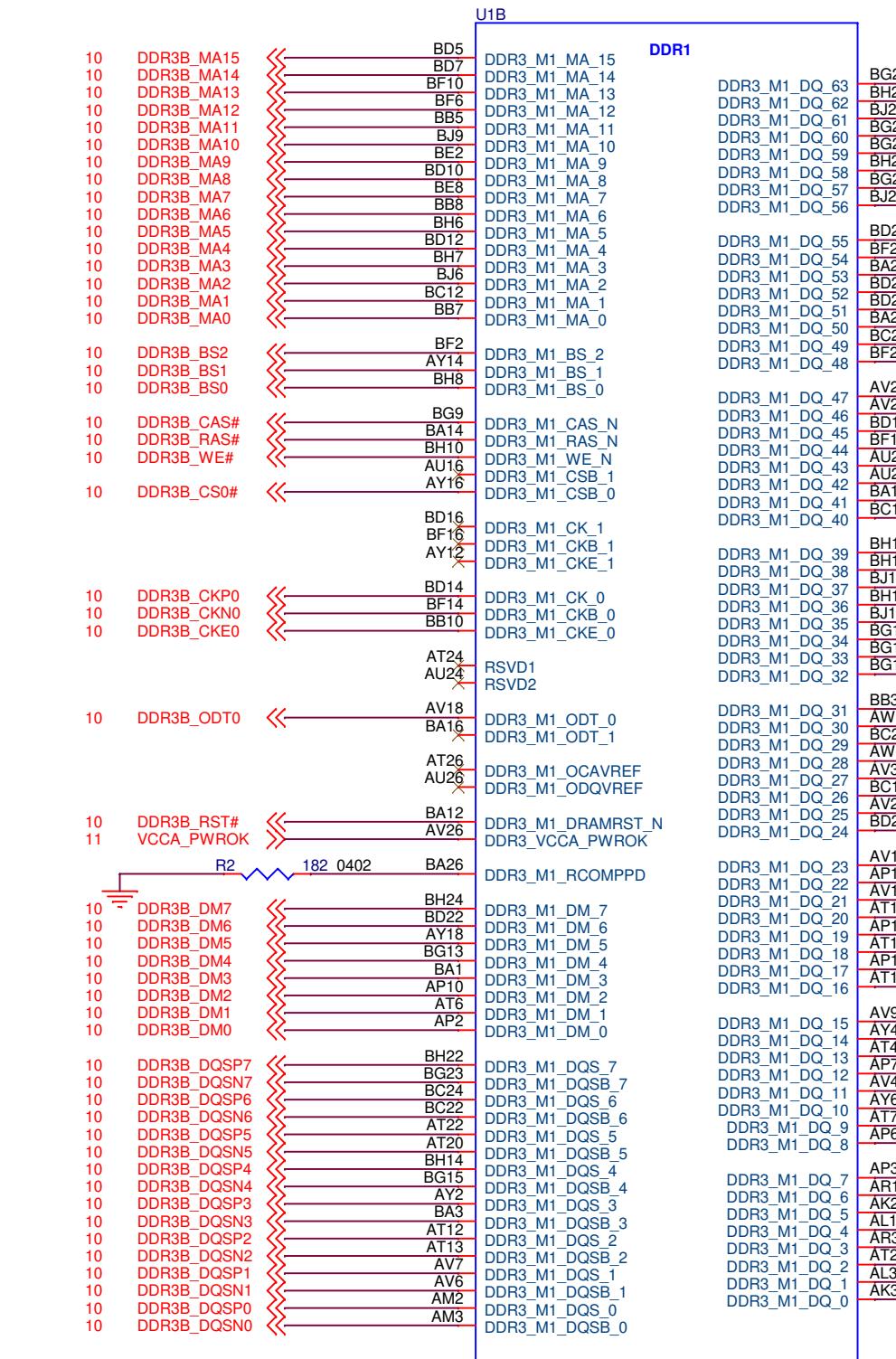
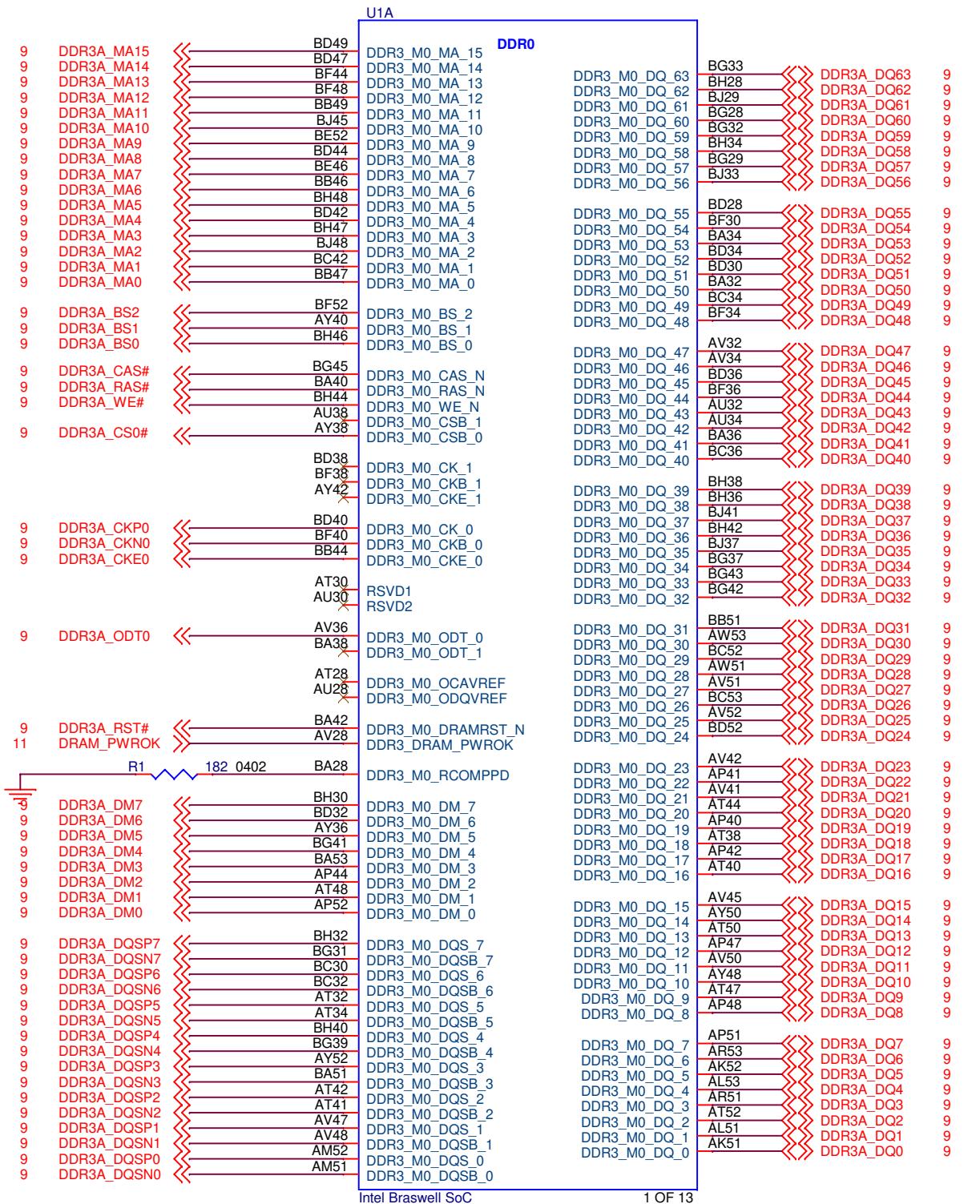
Components with CFG indications have to be mounted only on specific board's configurations (see table in this same page)



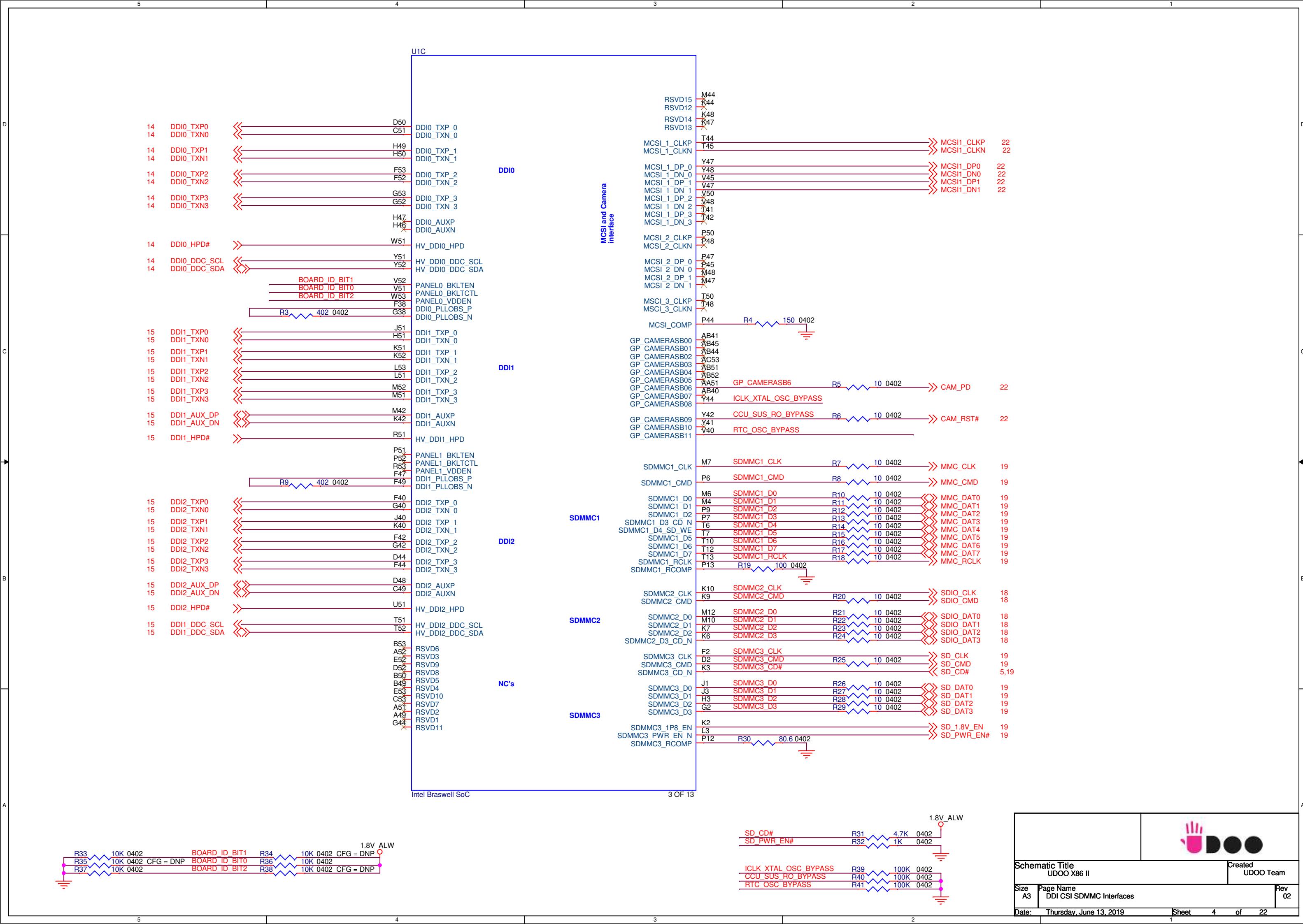
	
Schematic Title	Created
UDOO X86 II	UDOO Team
Size A3	Page Name MAIN
Date: Thursday, June 13, 2019	Rev 02

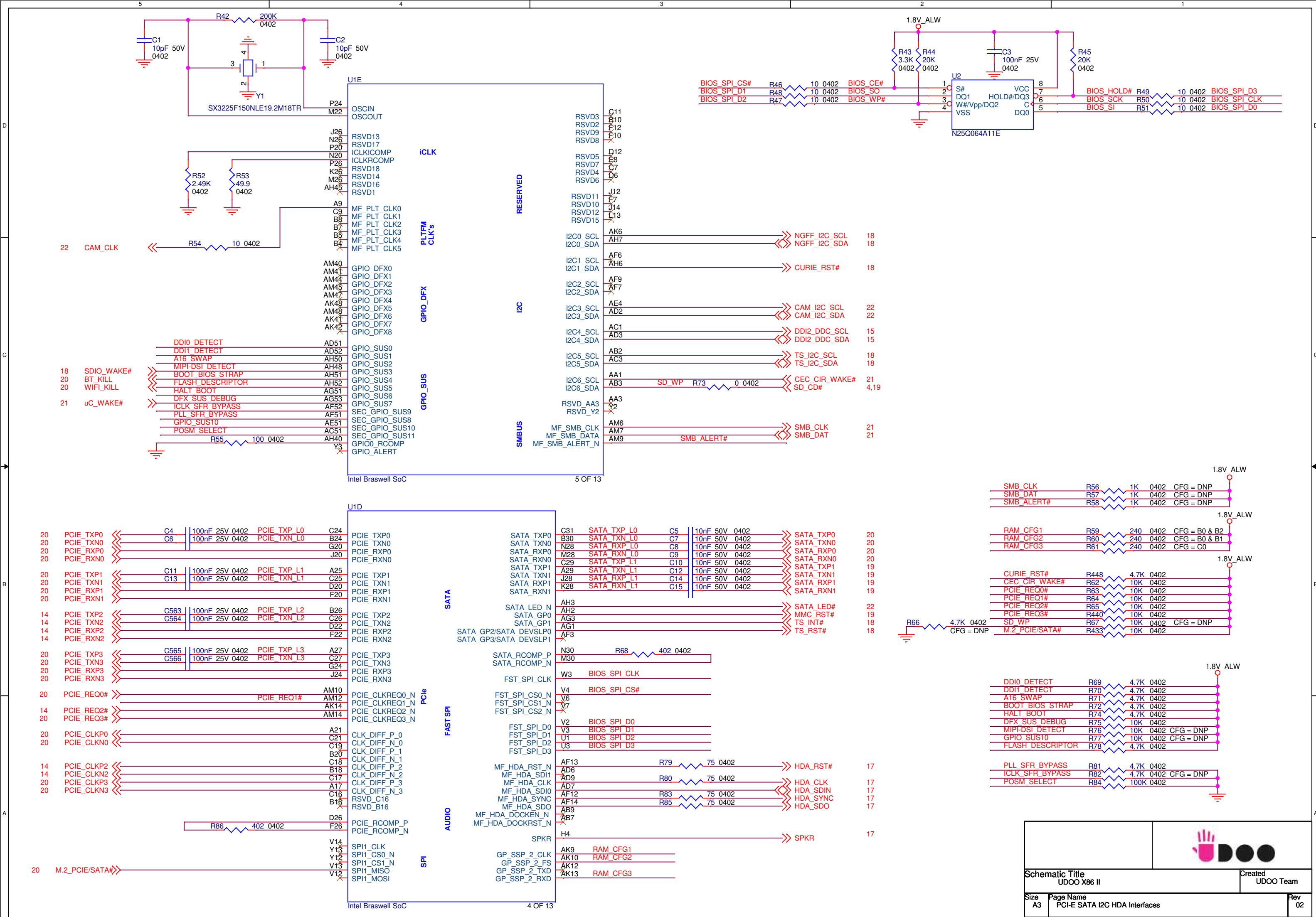


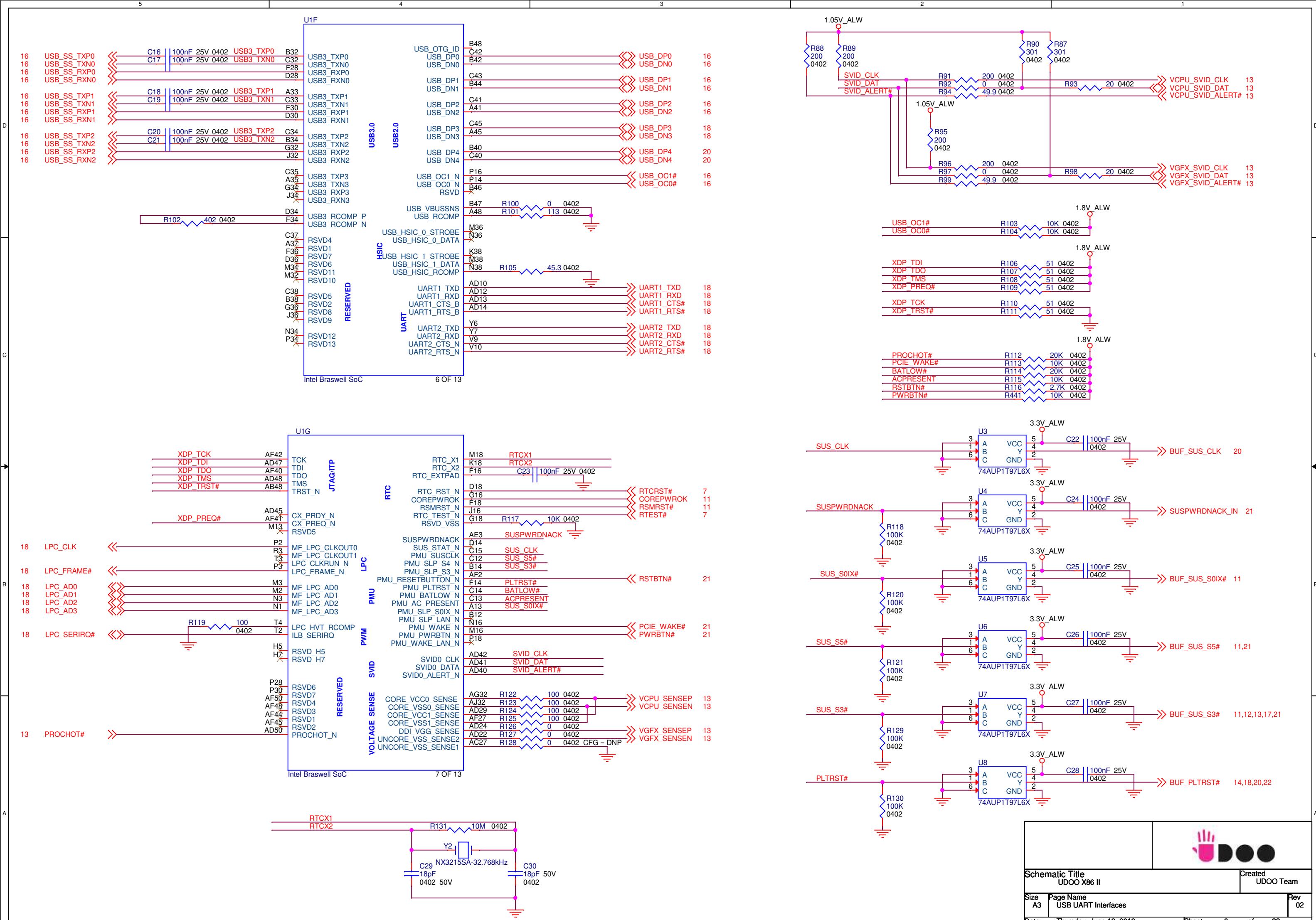
Schematic Title	UDOO X86 II
Created	UDOO Team
Size	A3
Page Name	Block Diagram
Date:	Thursday, June 13, 2019
Rev	02
Sheet	2
of	22

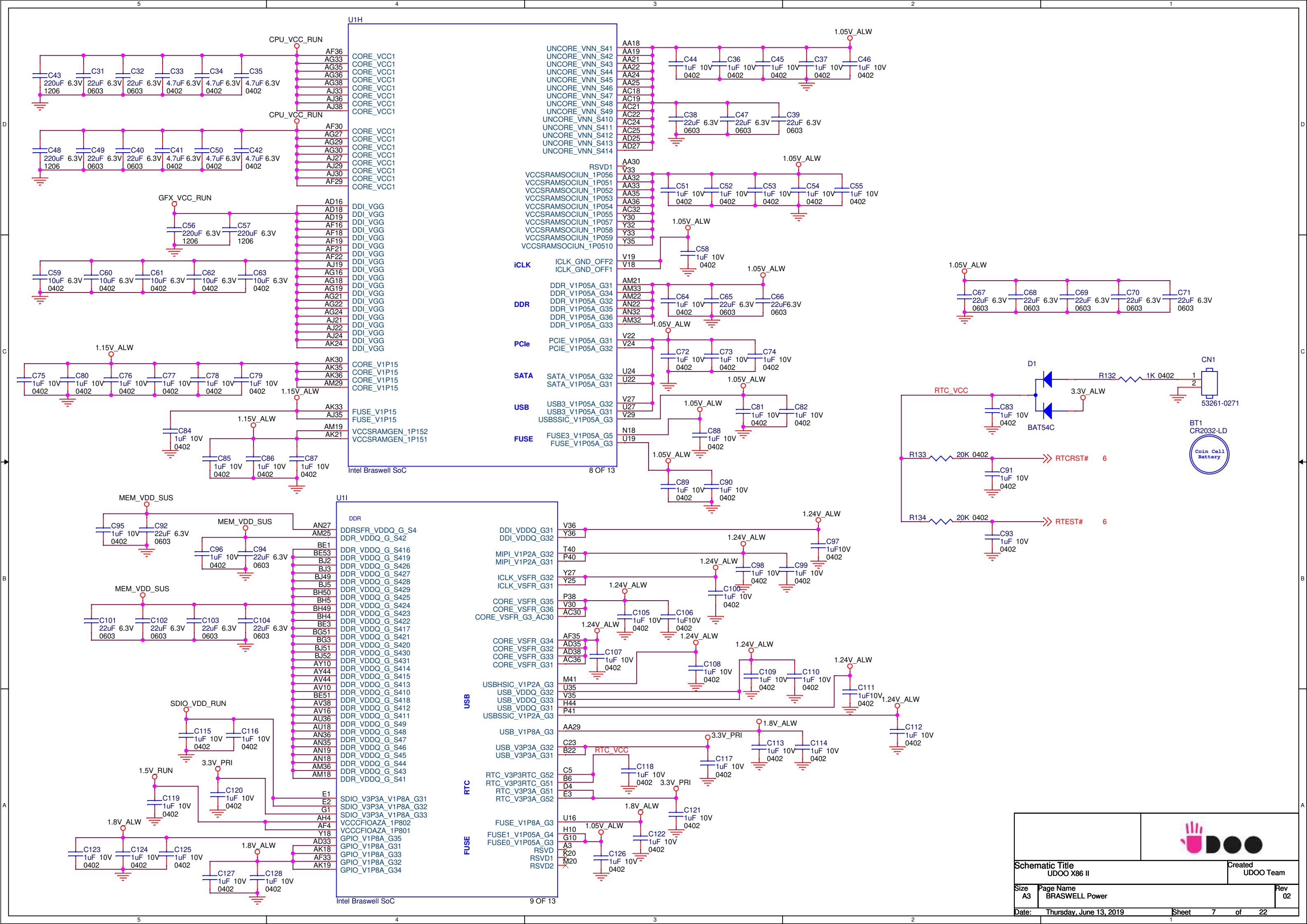


Schematic Title UDOO X86 II	Created UDOO Team
Size A3	Page Name DDR Interface
Date: Thursday, June 13, 2019	Sheet 3 of 22





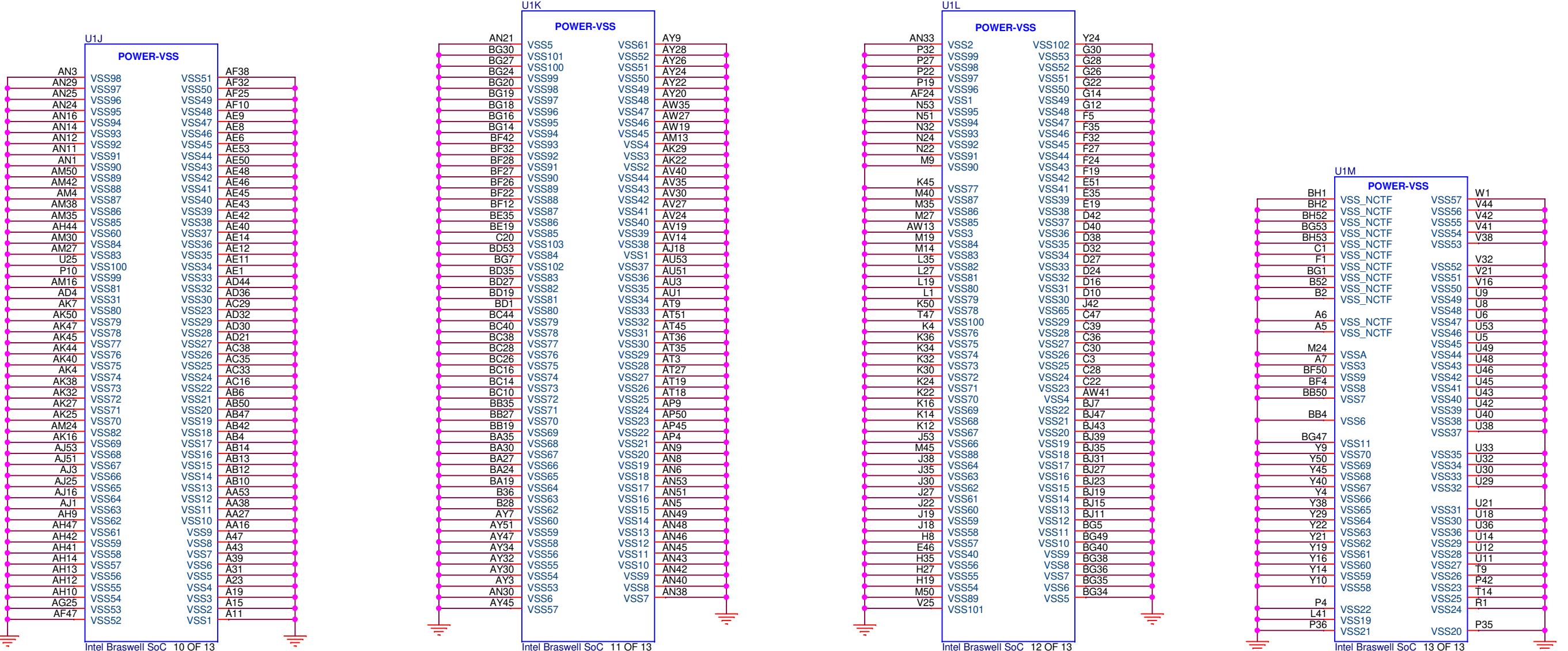




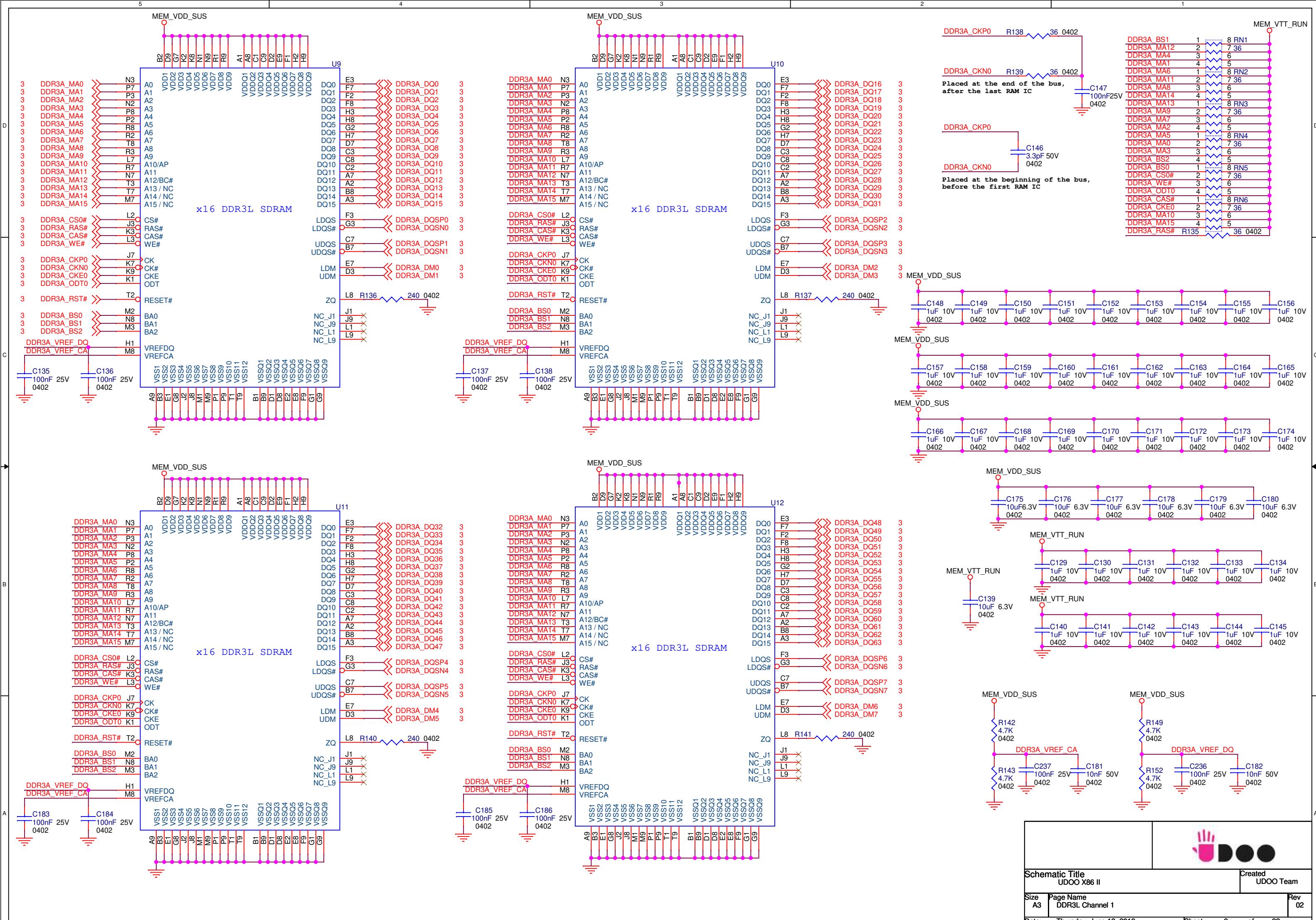
Schematic Title UDOO X86 II  
Created UDOO Team

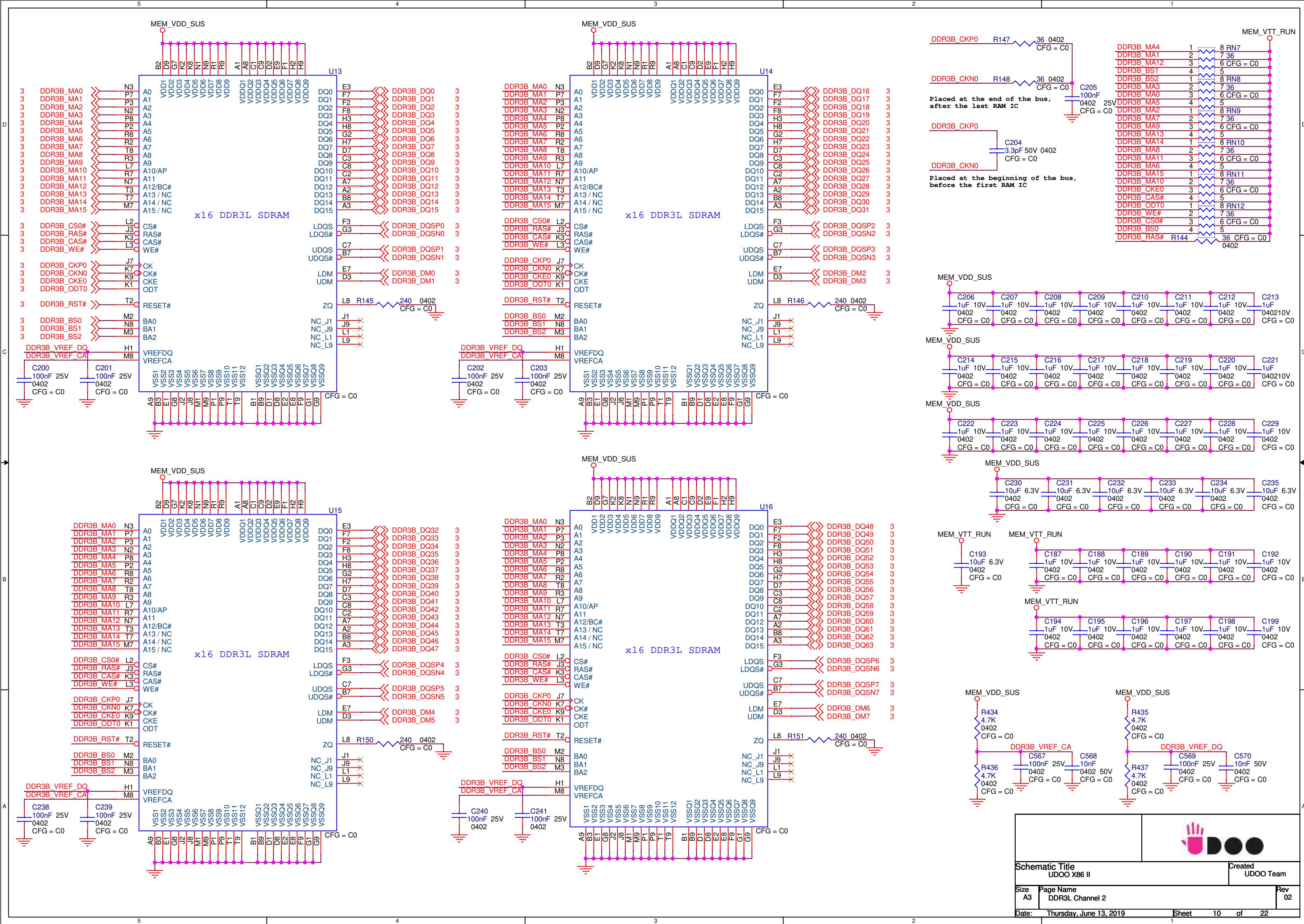
Size A3 Page Name BRASWELL Power Rev 02

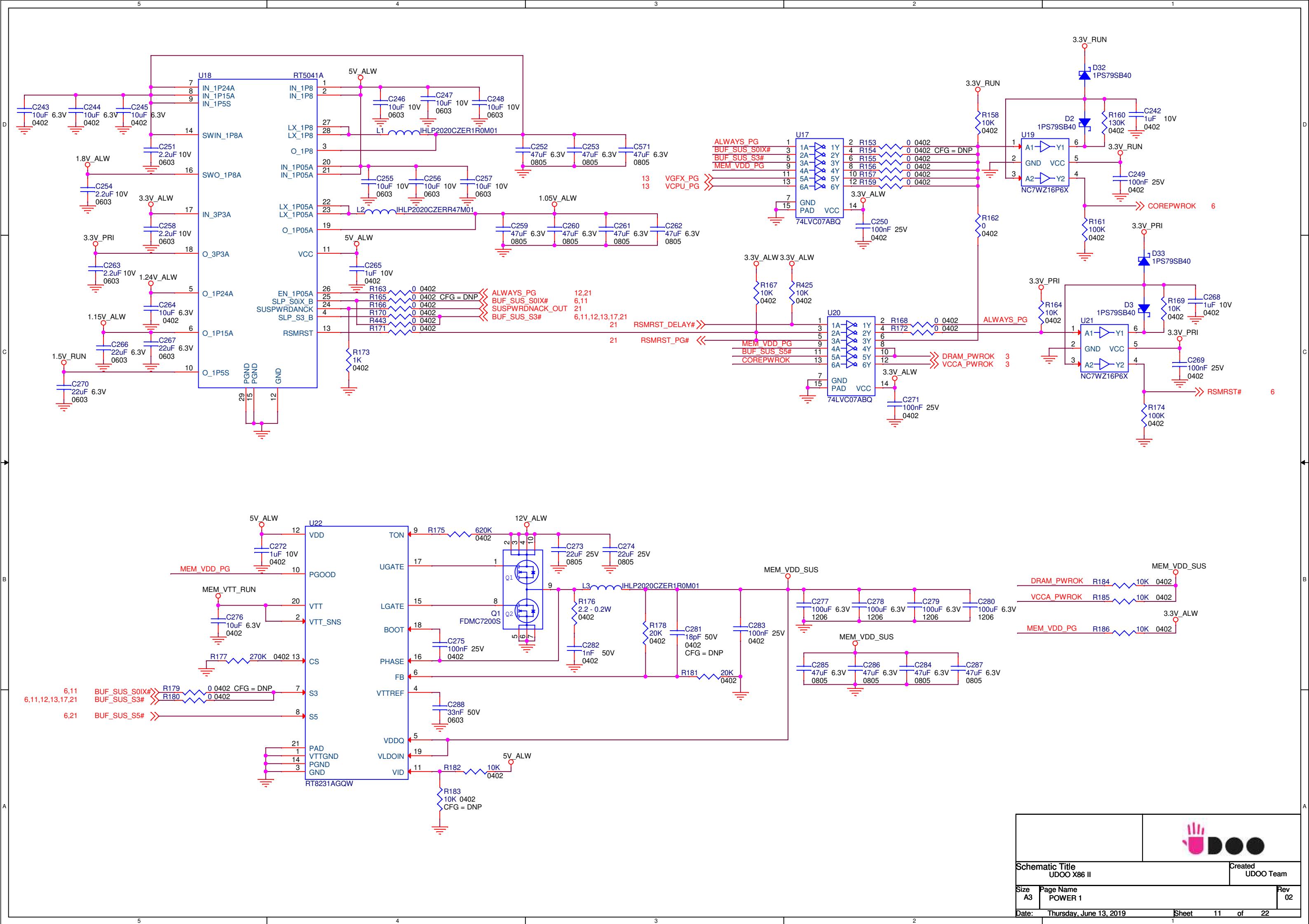
Date: Thursday, June 13, 2019 Sheet 7 of 22

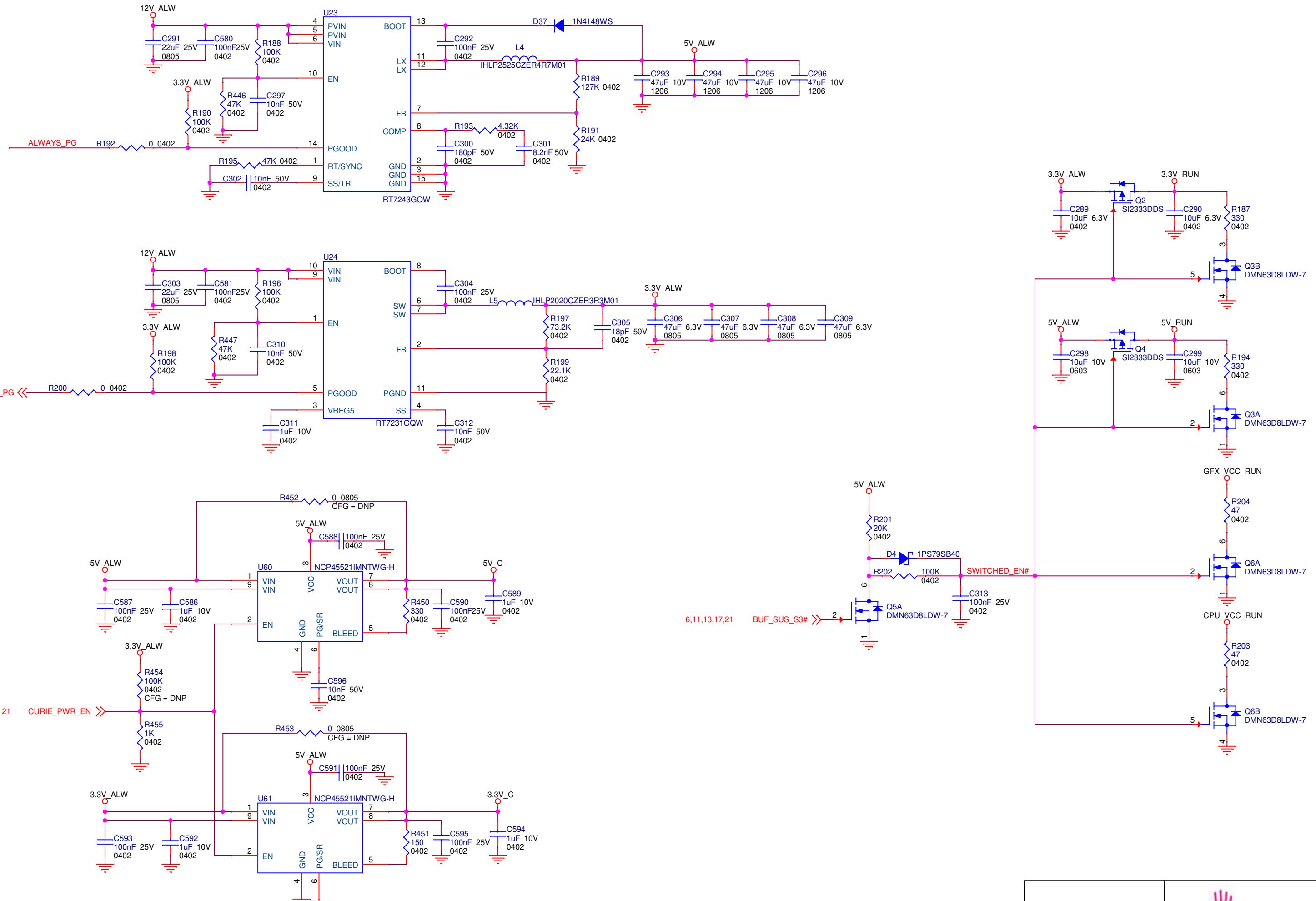


Schematic Title	UDOO X86 II	Created UDOO Team
Size	Page Name BRASWELL GND Pins	Rev 02
Date:	Thursday, June 13, 2019	Sheet 8 of 22

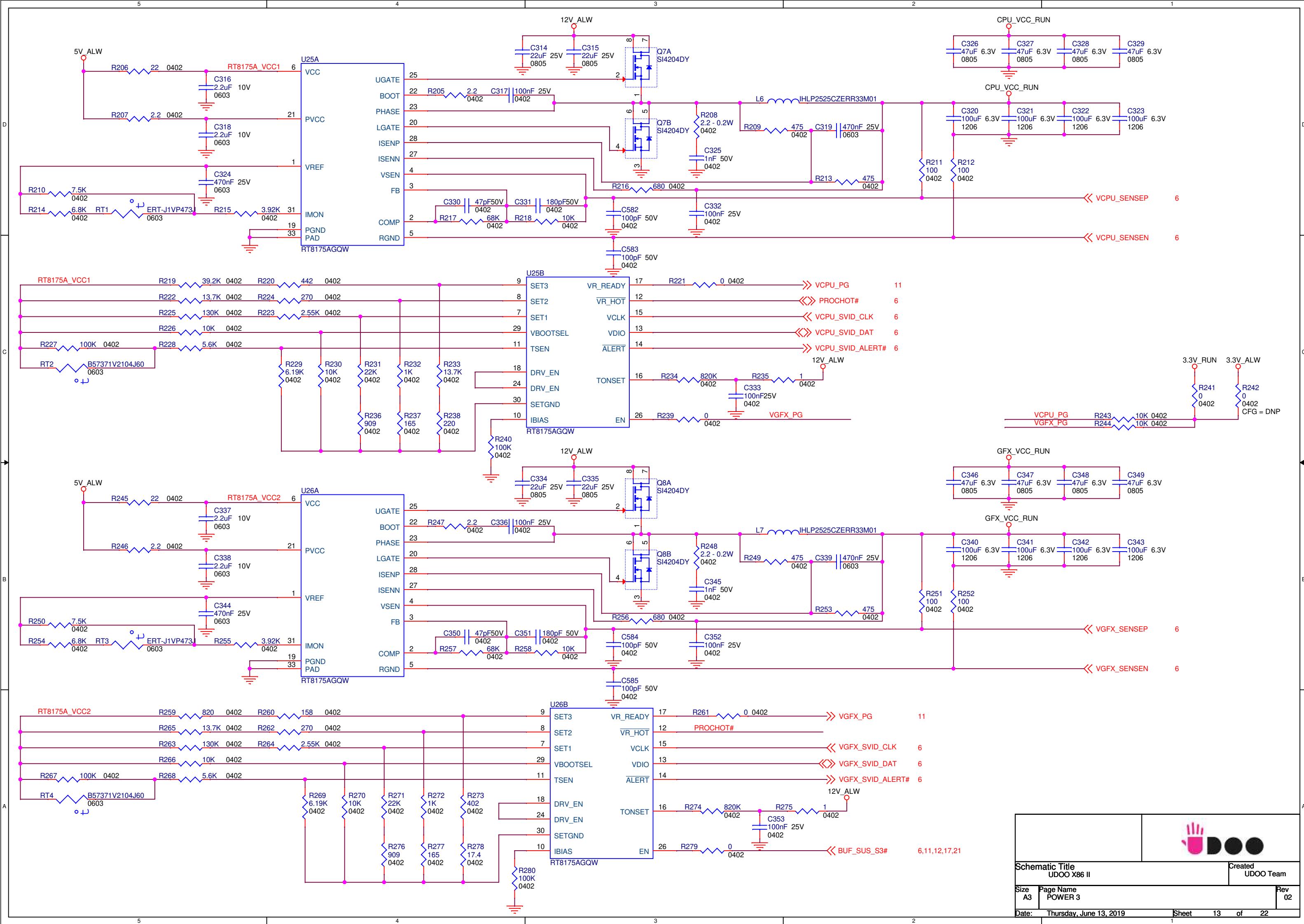


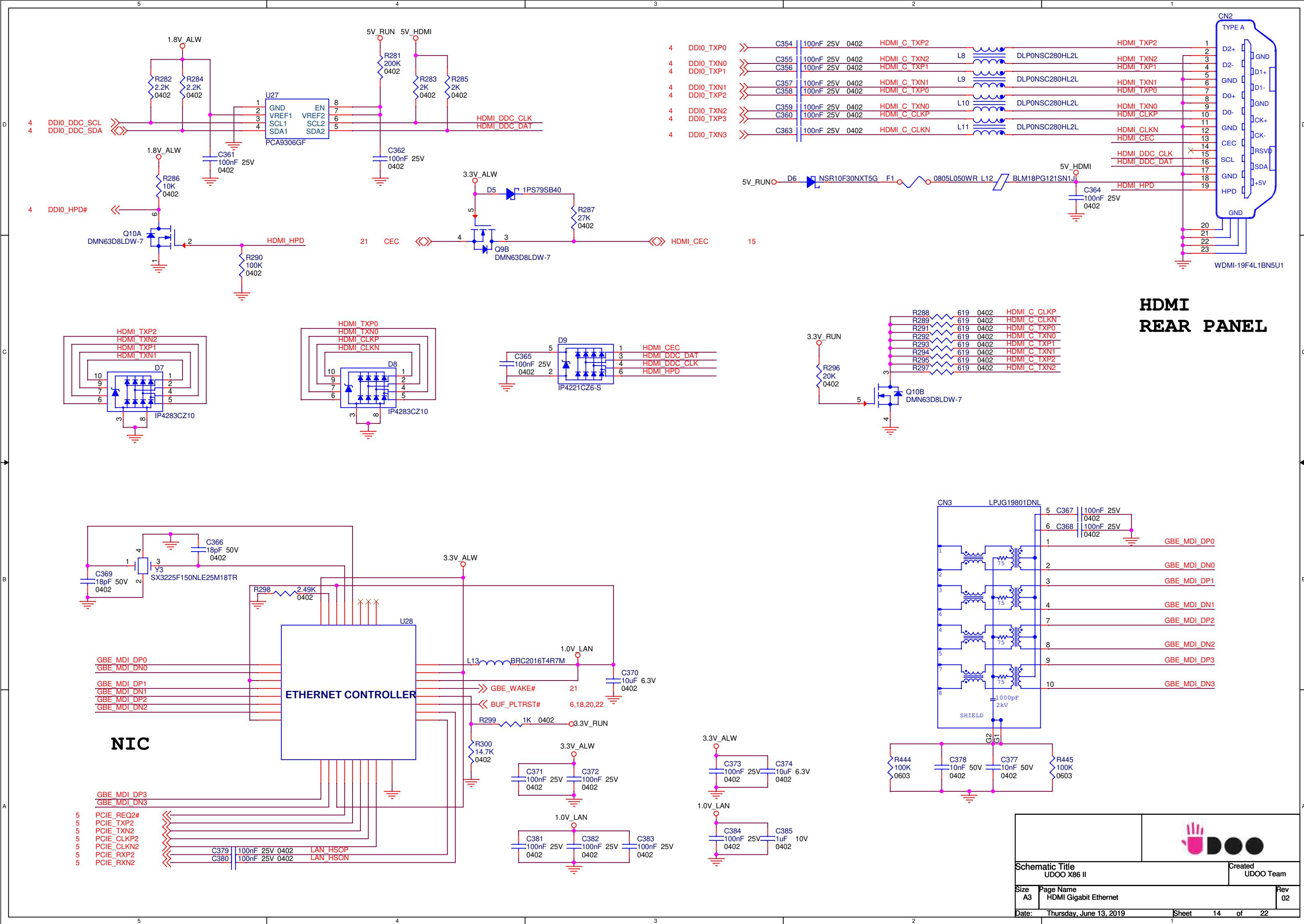


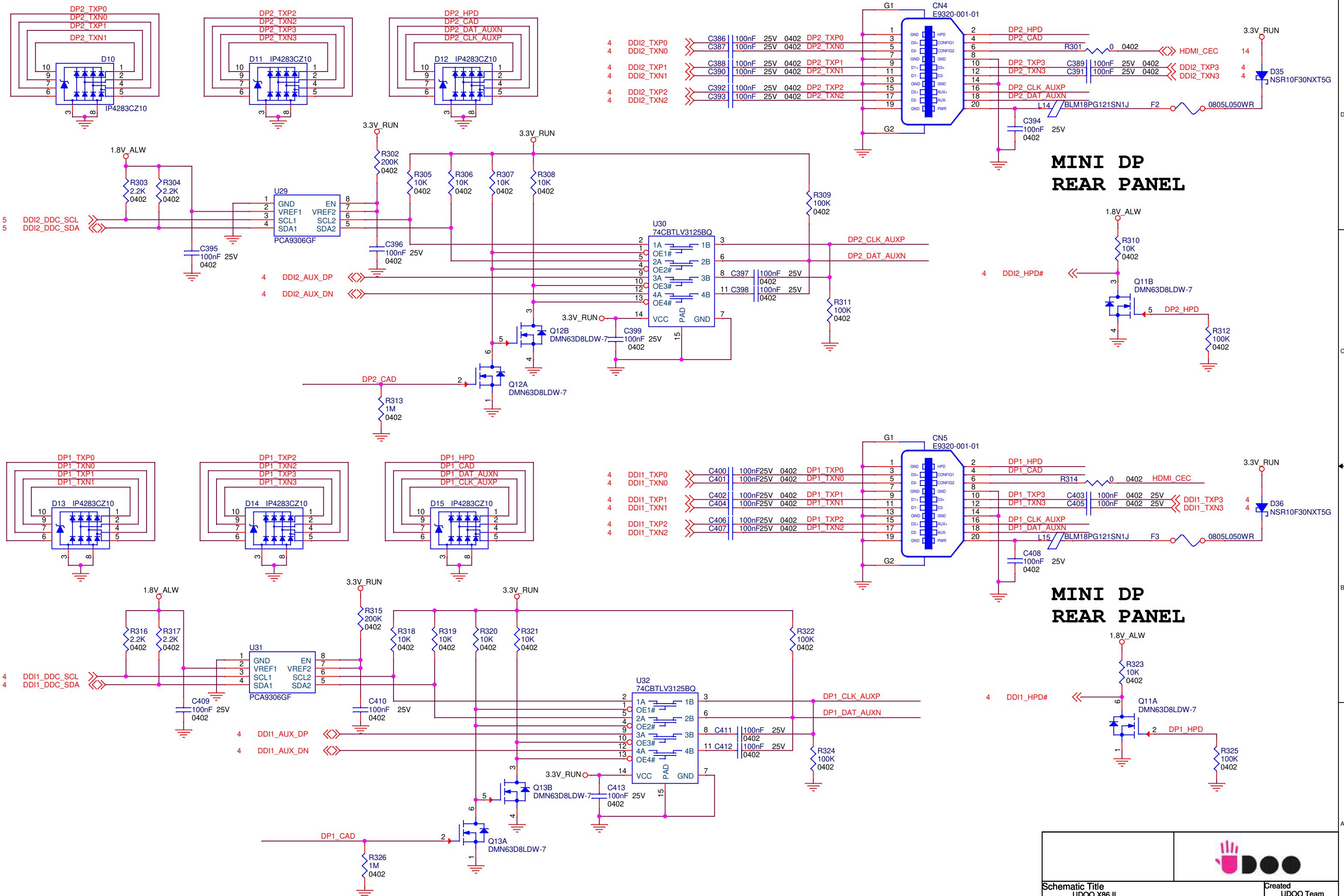




Schematic Title	UDOO X86 II	Created
Size	Page Name	UDOO Team
A3	POWER 2	Rev 02
Date: Thursday, June 13, 2019		Sheet 12 of 22

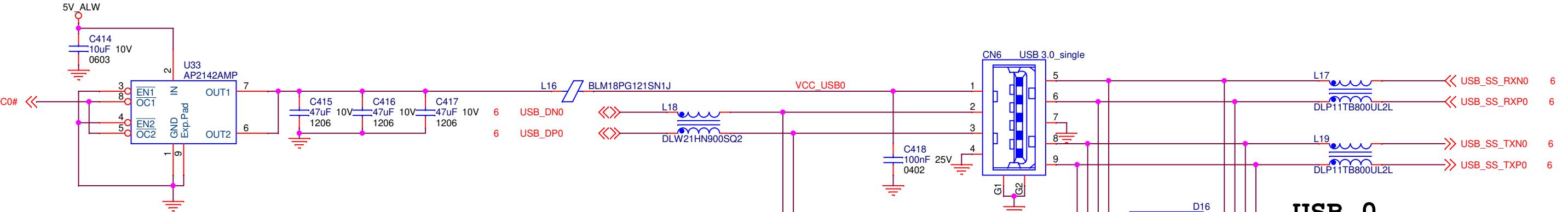




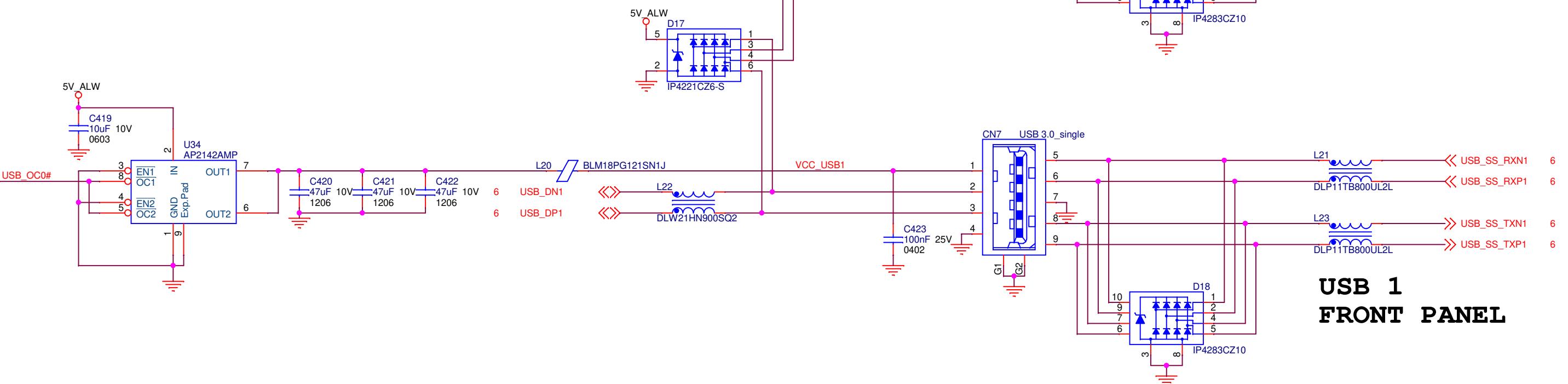


Schematic Title UDOO X86 II	Created UDOO Team
Size A3	Page Name mini DP++ connectors
Date: Thursday, June 13, 2019	Rev 02

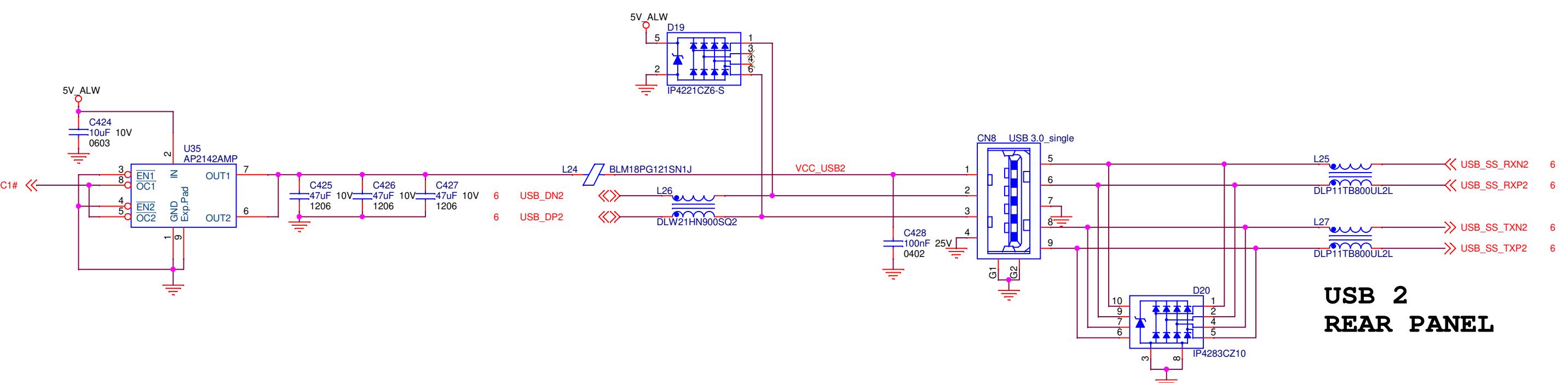




**USB 0  
FRONT PANEL**

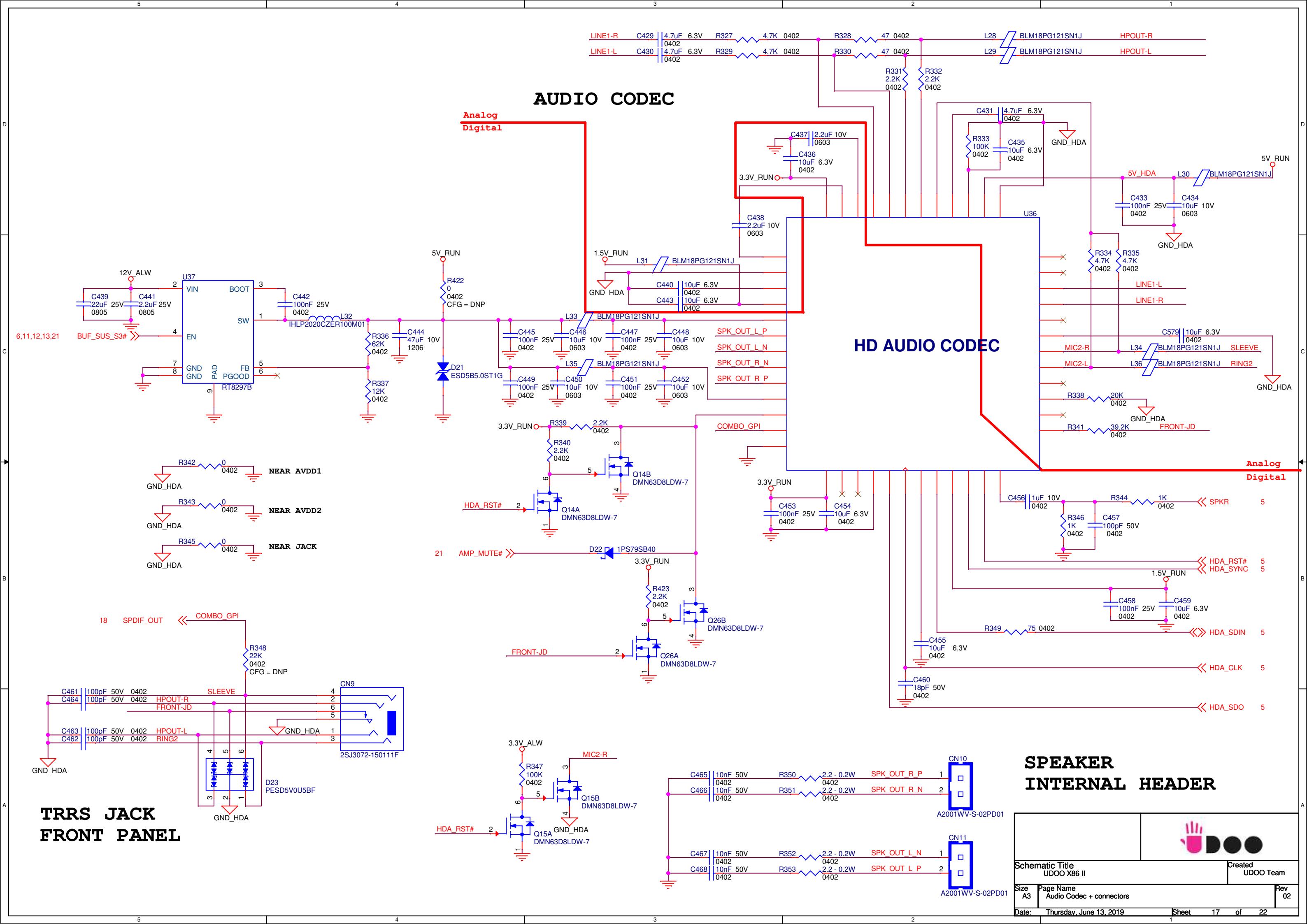


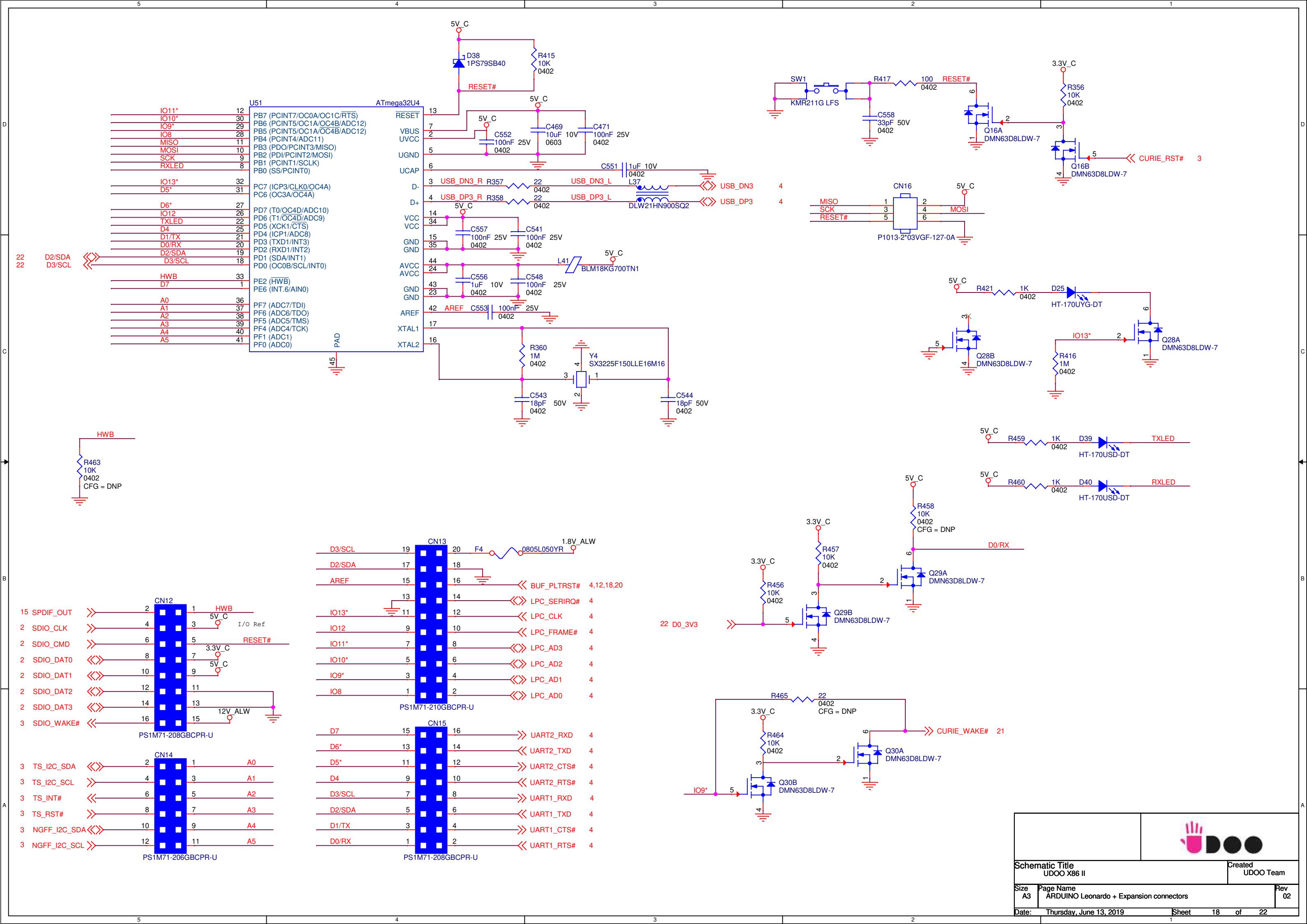
**USB 1  
FRONT PANEL**



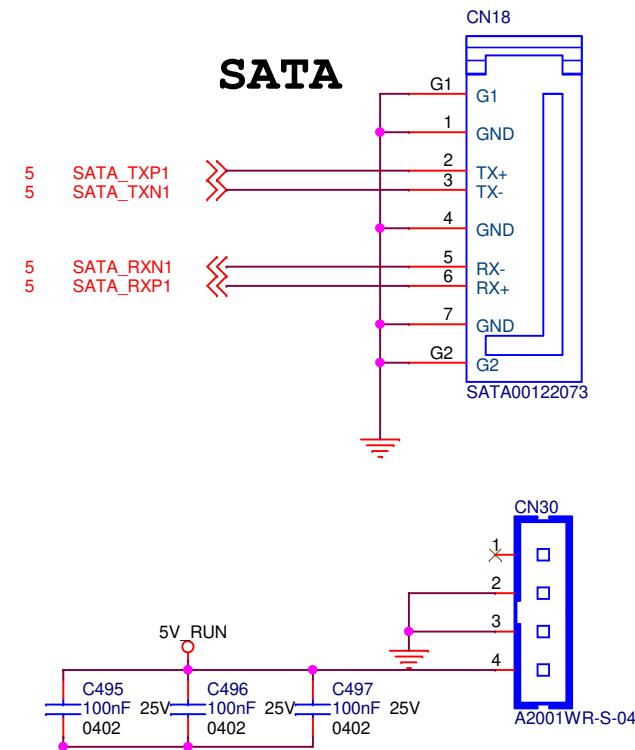
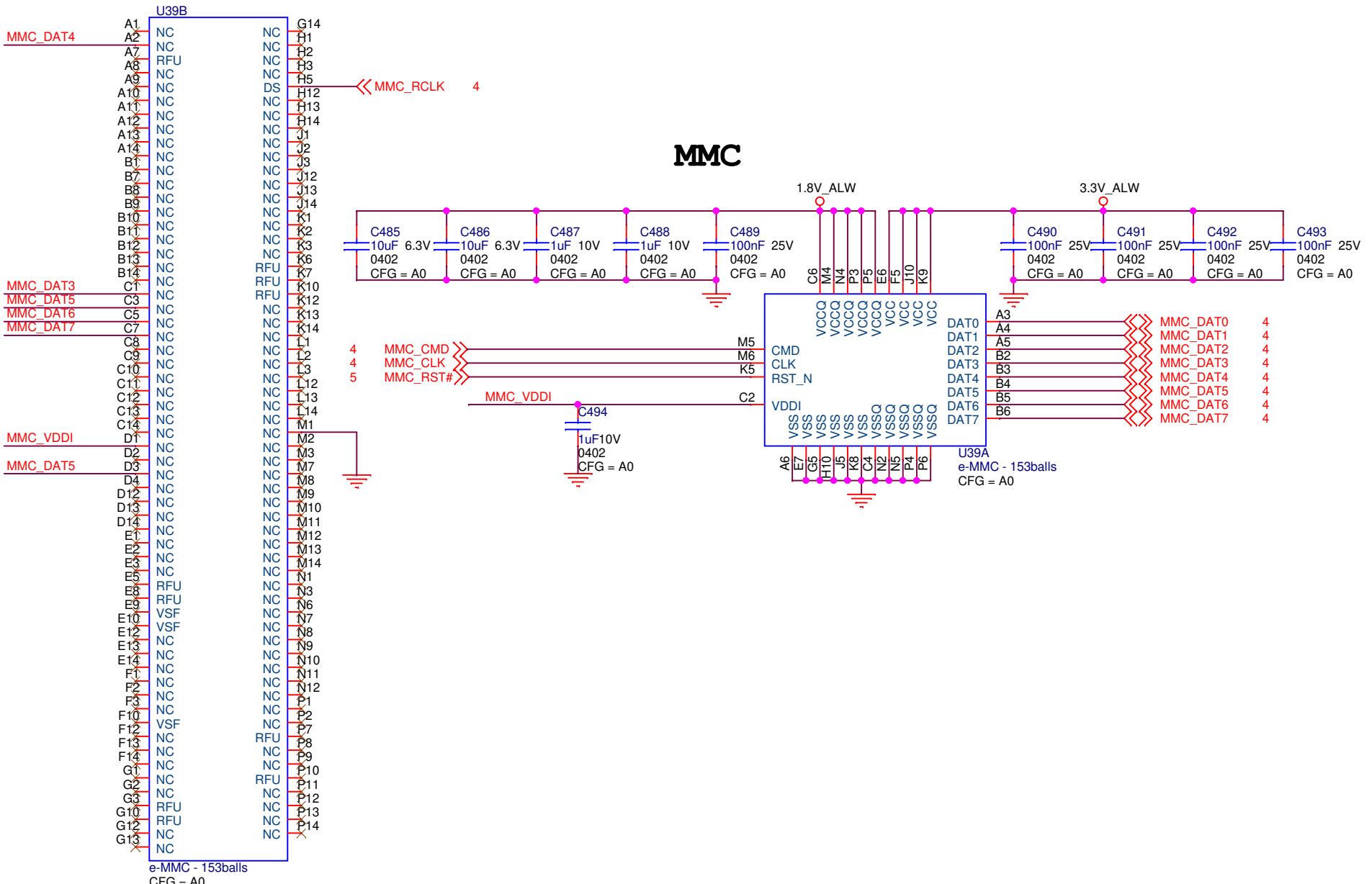
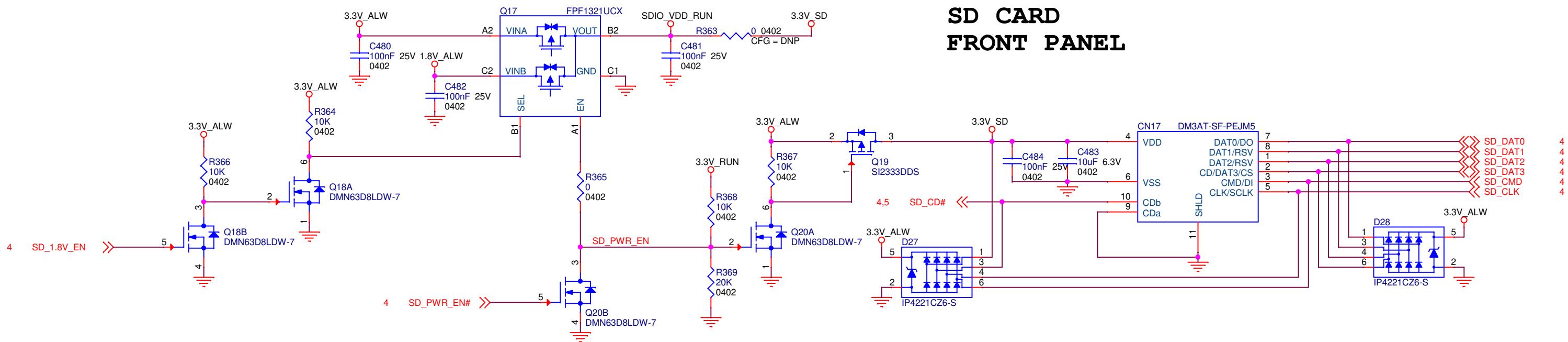
**USB 2  
REAR PANEL**

Schematic Title	UDOO X86 II
Created	UDOO Team
Size	A3
Page Name	USB 3.0 connectors
Date:	Thursday, June 13, 2019
Rev	02





# SD CARD FRONT PANEL

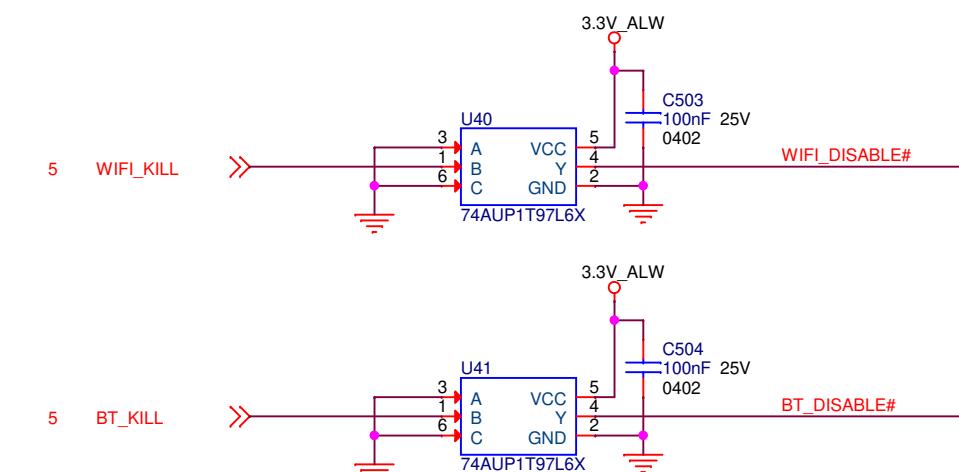
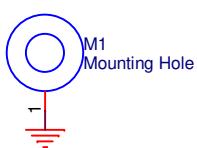
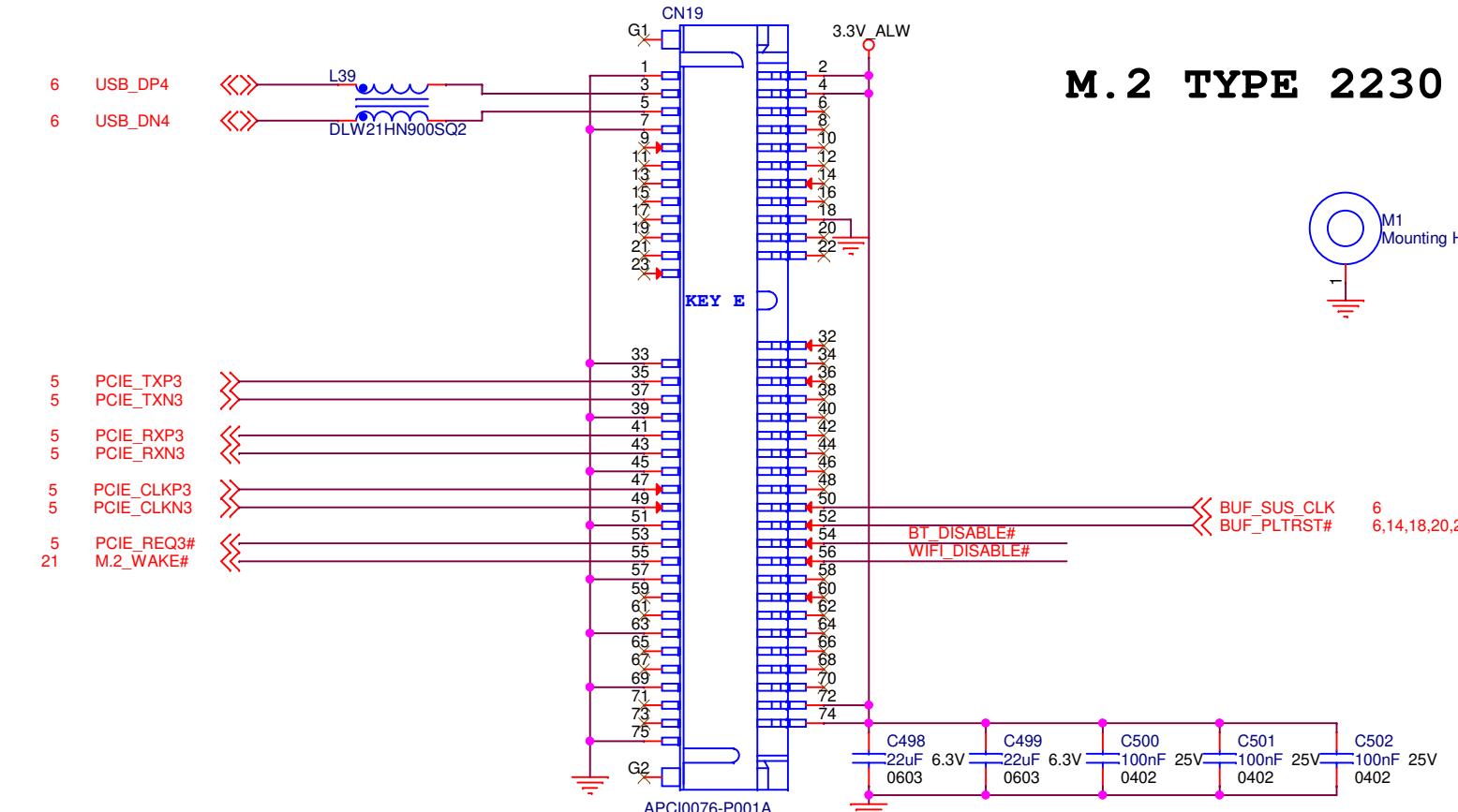


Schematic Title	UDOO X86 II	Created UDOO Team
Size	Page Name	Rev
A3	eMMC, microSD Slot, SATA connector	02

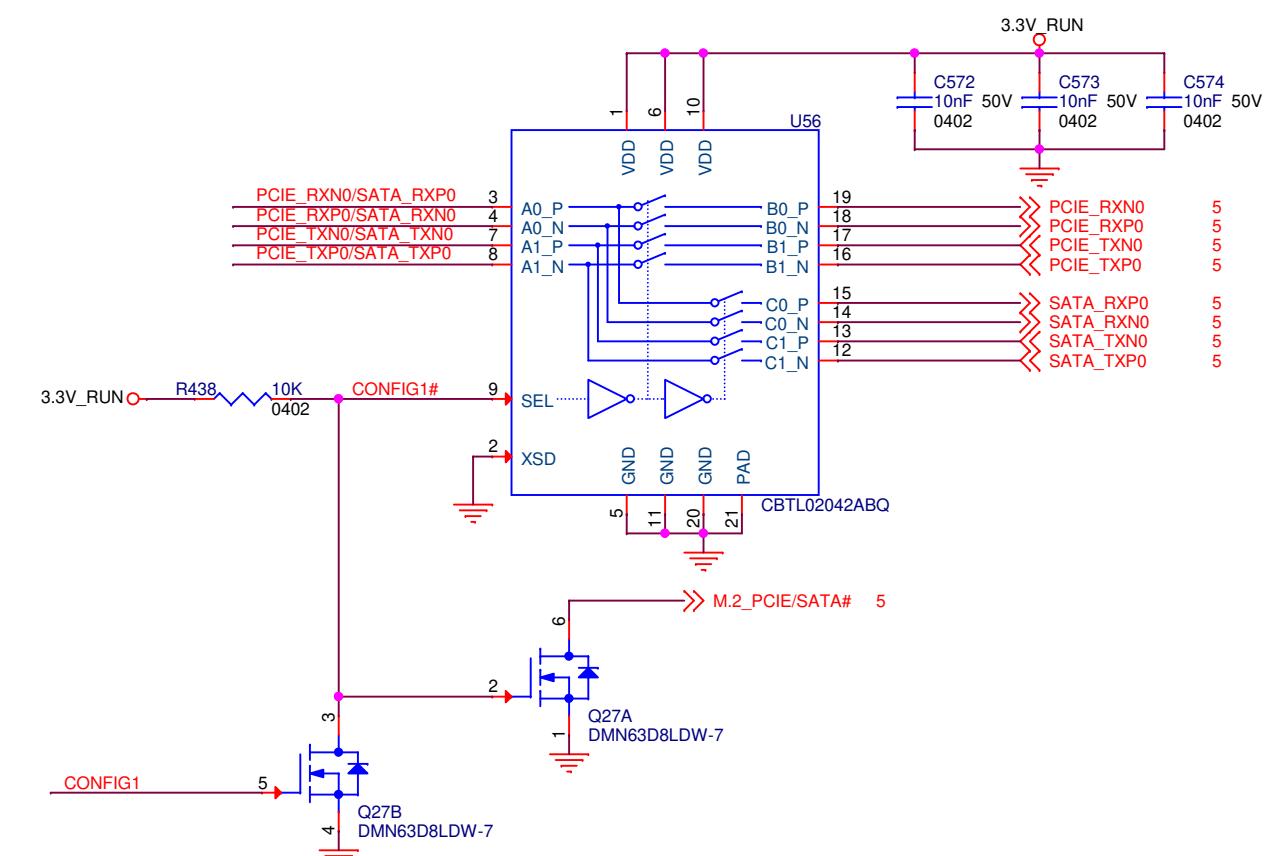
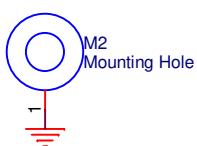
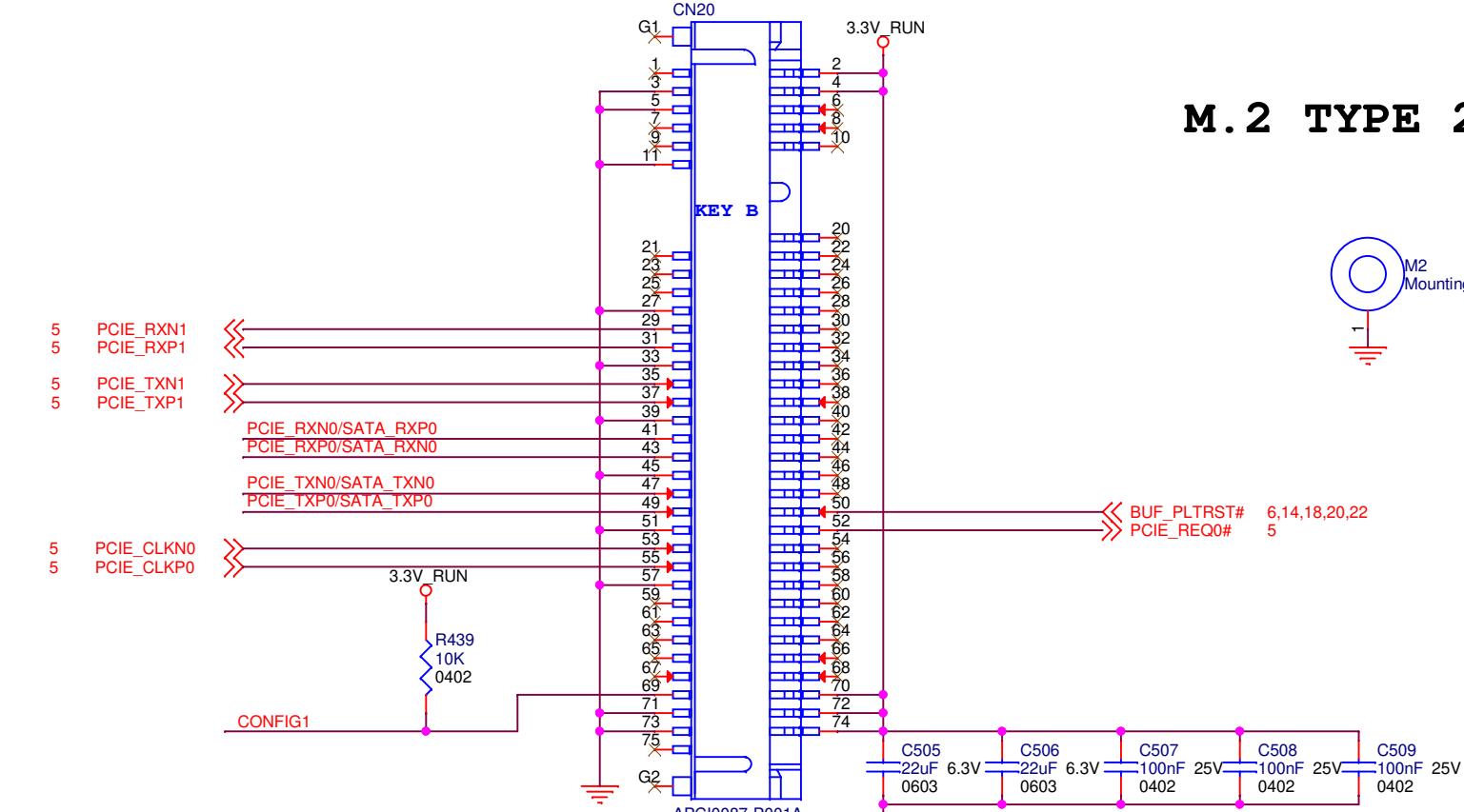
Date: Thursday, June 13, 2019

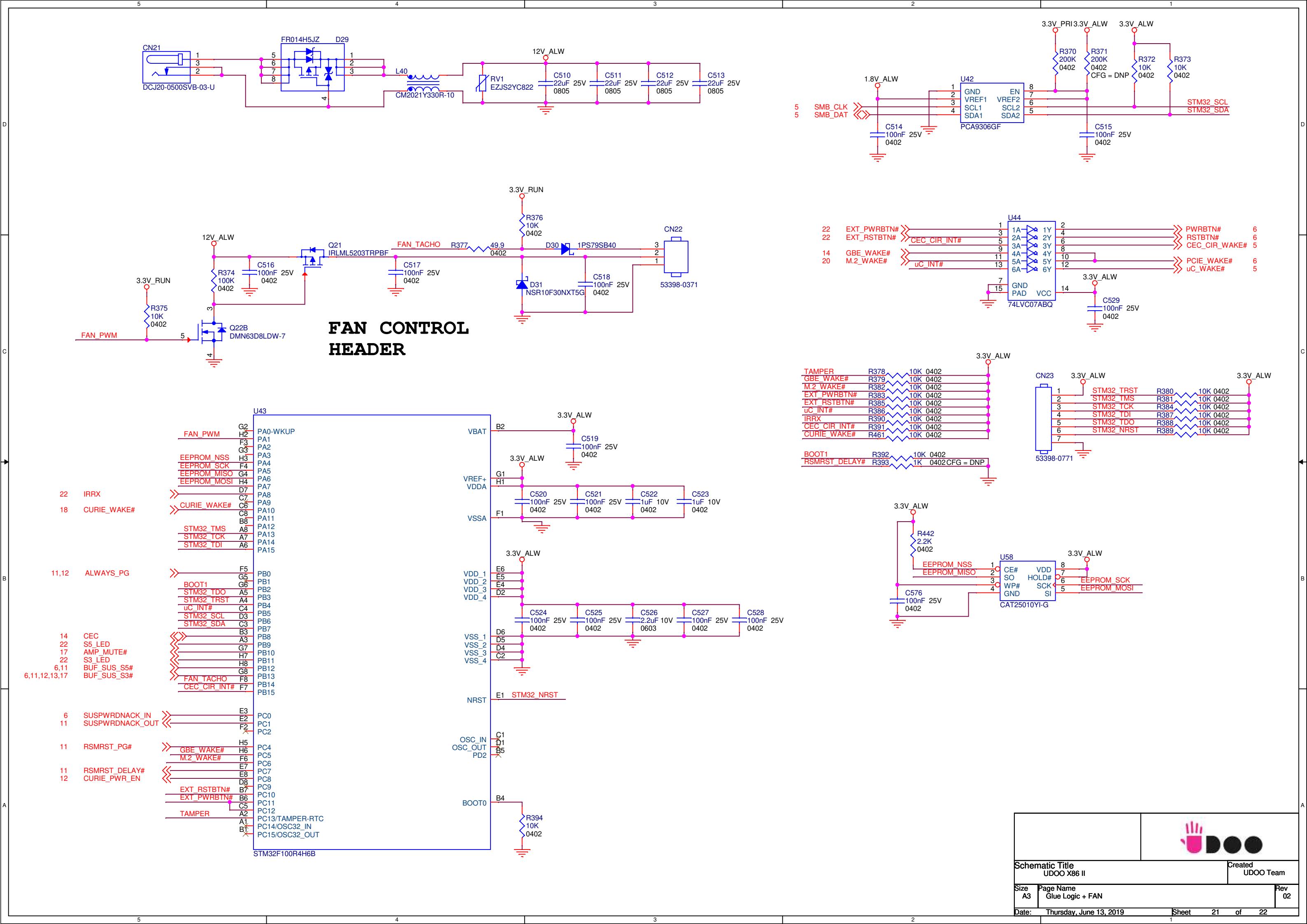
Sheet 19 of 22

## M.2 TYPE 2230



## M.2 TYPE 2260

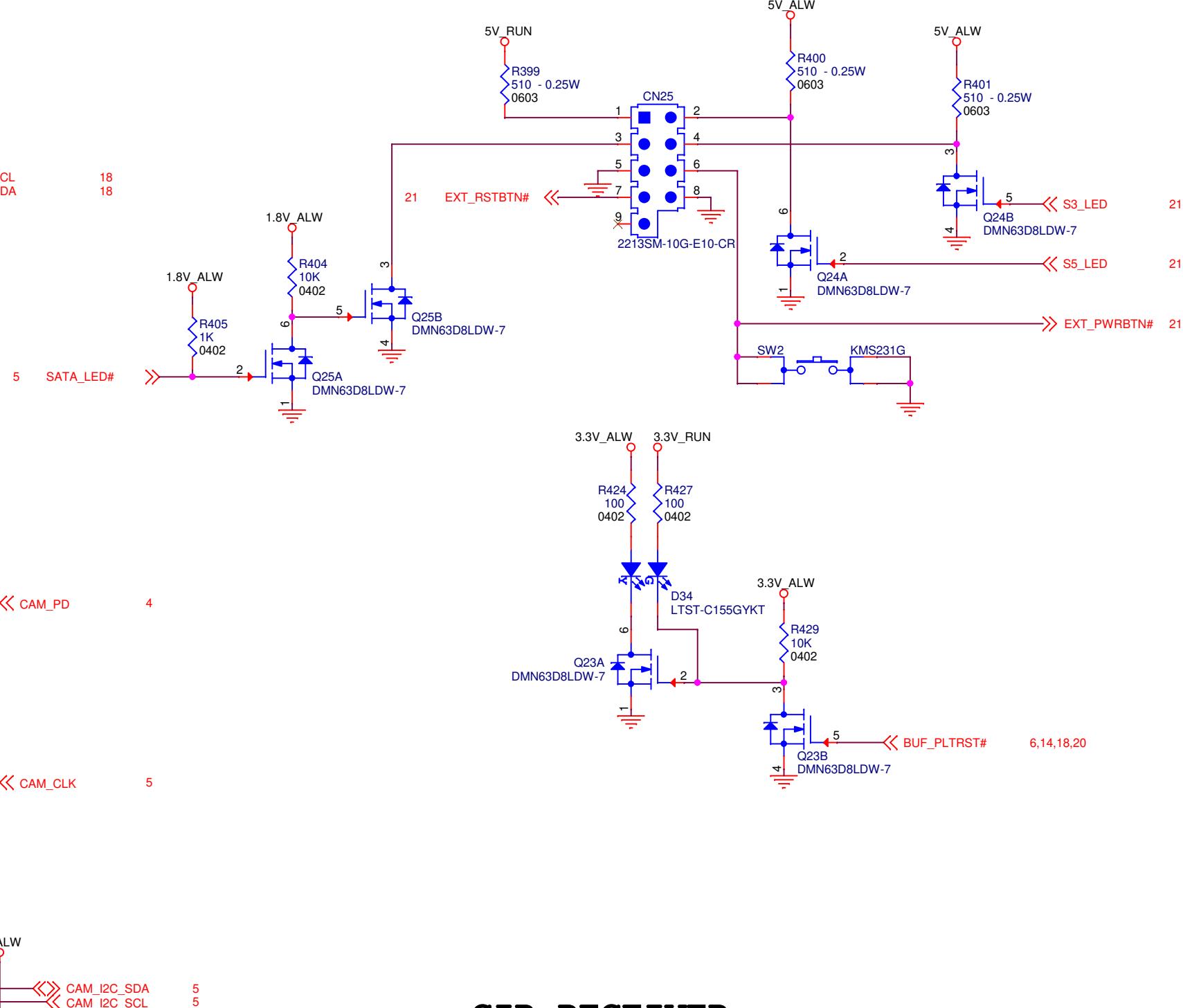
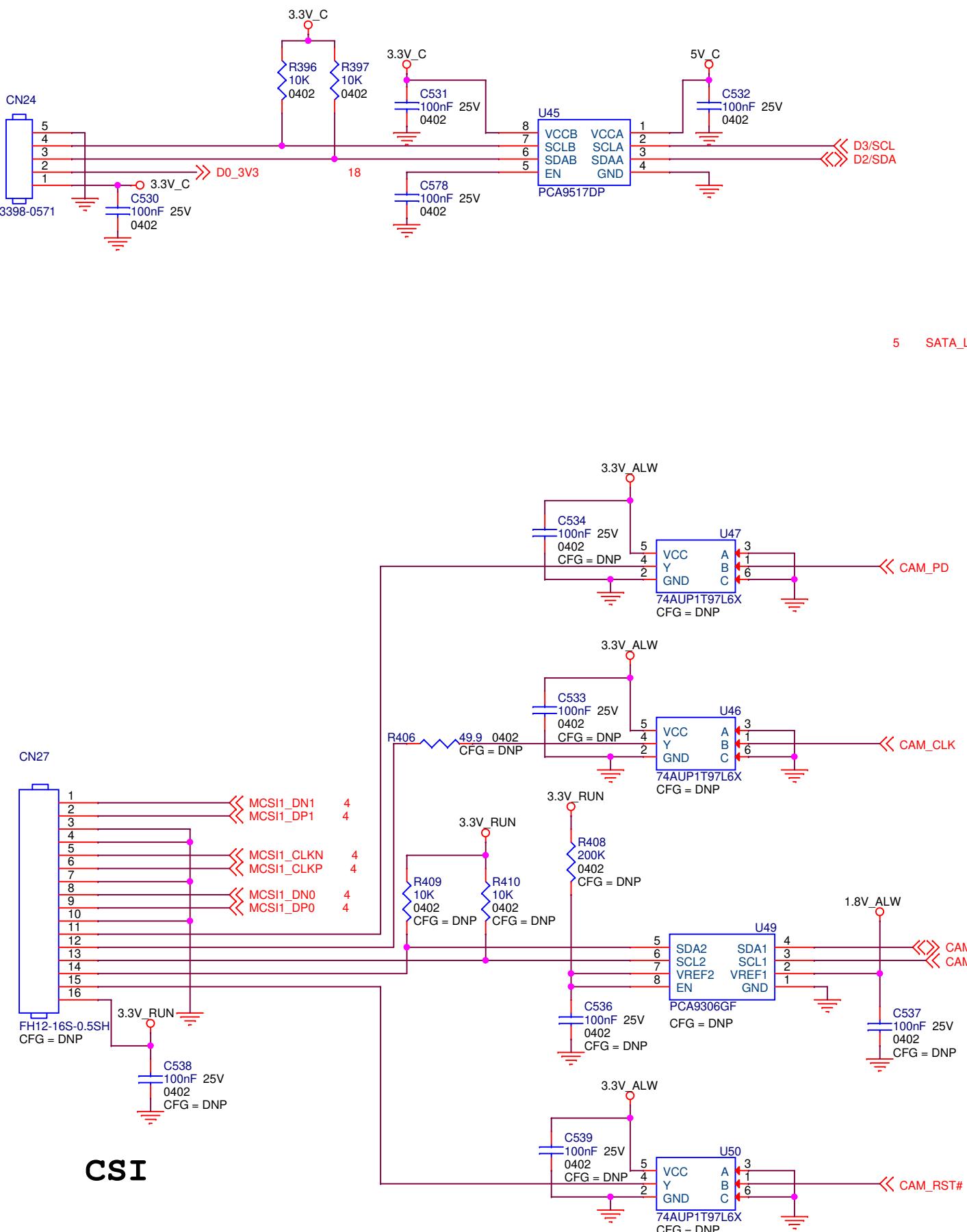




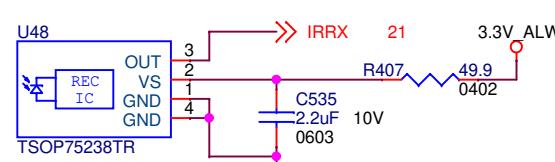
Schematic Title UDOO X86 II	Created UDOO Team
Size A3	Page Name Glue Logic + FAN
Date: Thursday, June 13, 2019	Rev 02

# **FRONT PANEL INTERNAL HEADER**

# **BRICK SUPPORT**



## CIR RECEIVER



Schematic Title UDO0 X86 II		Created UDO0 Team
Size A3	Page Name Front Panel, Brick, CIR, CSI	Rev C
Date:	Thursday, June 13, 2019	Sheet 22 of 22